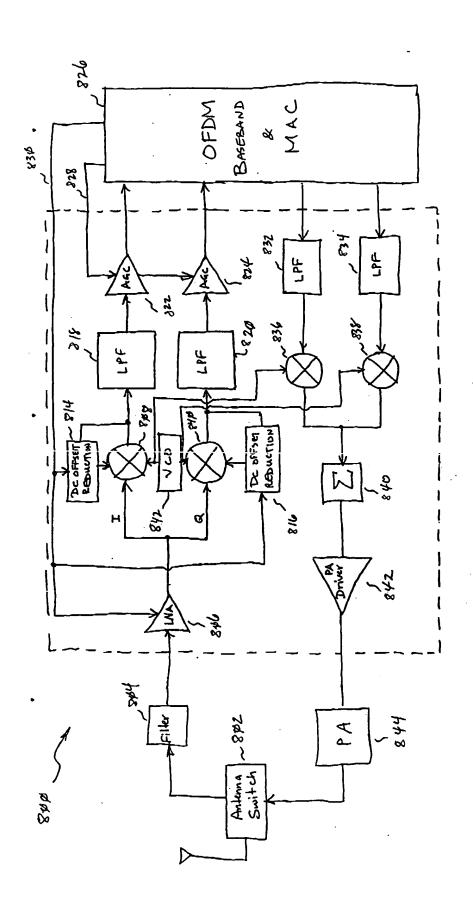


MEASURING FREE - RUNNING FREQUENCY OF A VLO A PLURALITY GENERATE CONTROL LOGIC SIGNALS APPLY CONTROL LOVIC SIGNALS to 1" PLURALITY OF RESISTORS, EACH COUPLED to MID POINT A SERIES OF TWO CAPACITORS EACH SERIES COUPLED BETWEEN A UCO AND FIRST NODE A NODE CONTROL VOLTAGE to APPLY AT LEAST ONE RESISTOR COSPLED OF A SERIES CAPACITORS EACH COUPLED RÉMEEN AND A SECOND NODE

FIGURE 7



FIGURES

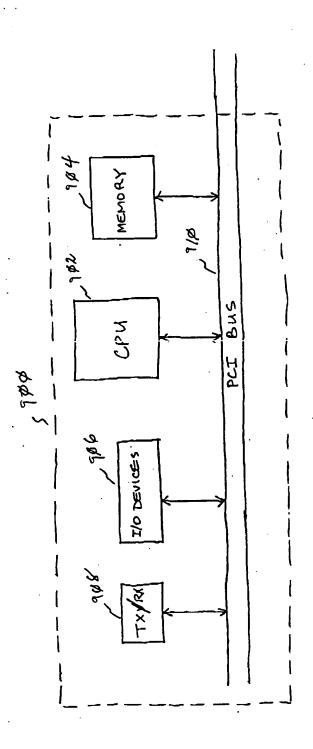


FIGURE 9